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# Output-Input Hybrid Passive Filter Design for Electric Vehicle Charging Station

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#### ABSTRACT

The impact of electric car charging stations on power grid harmonics is growing as their existence continues to expand and creates harmonic distortion in the power system. At the same time, the quality of charging current and voltage through the process of charge becomes inferior. Therefore, reduction of output ripple factor (RF) and mitigation of input current harmonics is vital in this situation. This paper presents the design of the output filter and three kinds of shunt passive input filters to decrease the present harmonics generated in the industrial power scheme by the charging stations. The simulation software MATLAB/Simulink was used. Results were acquired before and after installation of filters and after installation of found that the percentage RF of output current, voltage, and the percentage THD of the input current is reduced to match the set standards.

# Keywords:

Electric Vehicle Charger; Charging Station; Passive Filters; Total Harmonic Distortion; Current Distortion

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# 1. INTRODUCTION

Countries have started to create electric vehicles due to rising environmental pollution and increasing resource shortages, which require the development of comparable instruments, such as EV charging stations [1][2]. The growing presentation of non-linear equipment in the form of charging stations will result in significant harmonious grid pollution [2][3][4]. This can lead to some serious effects such as enhanced network losses and voltage fluctuations, heating issues and electrical equipment efficiency losses, power supply interruptions, or even large blackouts[3][4][5][6]. These power supply infrastructure efficiency losses will also impact the life cycle of alternative electric cars, which is crucial for choosing alternative technologies with distinct routes[7][8]. The electric vehicle companies have therefore implemented requirements for electric vehicles ' voltage and current harmonic limits[9].

The harmonic pollution from a charging station for electric vehicles stems primarily from the charger's uncontrolled or regulated rectification circuit. The grid voltage will produce a pulsating Direct voltage (VDC) on the charger after rectification by a three-phase rectifier which resulted in the generation of DC current. Current harmonic distortion occurs on the side of the charging station's Alternating Current (AC) due to the interconnection between the power grid and rectifier. Typically, for high-rated chargers with regular and rapid charging kinds, they use a threephase rectification circuit[1][2][3]. During the process of charging, the stations are required to charge the battery in CC and CV modes with ripple factor percentage RF for the output of less than 3%[4]. Diverse types of filters are available (an example is a passive filter) concerning the harmonic requirements mitigation to reduce the percentage THD to <5. The primary benefits of passive filters are their low cost, uncomplicated design, and high reliability.[5]

Several studies have been carried out to reduce harmonics by using different kinds of passive filters[10][11][12][13]. While others studies focus on new methods for filter parameters calculation and combine passive filter with other techniques like active filters, fuzzy logics , etc.[14][15][16][17][18]

This paper analyses the current charging quality as well as current harmonics at the input of an electric vehicle charger with an uncontrolled three-phase rectification circuit. Furthermore, to improve the effectiveness and design of LC output filter with single-tuned STPF, double-tuned DTPF, and second-order high passes passive HTPF input filters. The investigation is directed toward reducing the output RF percentage and the harmonic current distortion THD percentage in the industrial power system.

#### 2. Ripple Current Mitigation

In most applications, the effect of ripple current should not exceed a specified value but must be mitigated if exceeded and the ripple effect from the DC output is reduced. Several methods can be used including raising the pulse number of rectifiers and the higher the number of the pulse, the lower the ripple magnitude[6]. Also, an output filter is used. If a capacitor is applied across the load and an inductor is used in series with the load, it would smoothen and lower the ripple effect[7].

The capacitor-inductor filter is used widely in high-power applications and it is connected as illustrated in Figure 1. Since there is no mathematical formula available to calculate the values of the capacitor and an inductor in capacitor-inductor filter[8][19], L and C are chosen by taking FFT for the output voltage or current then, XL = XC at the highest harmonic frequency. The Fourier series analysis for the output voltage was used to drive Equation 1:

$$\% RF = \frac{1}{6\sqrt{2}LC\omega^2} \tag{1}$$

Then to calculate L and C.



Fig. 1 LC filter connection.

#### 3. Harmonics Reduction

Passive filtering is the easiest standard option to mitigate harmonic distortion[20][19]. The passive filters use passive components such as resistance, inductance, and capacitance to regulate the harmonics. Common kinds of configurations for passive filters and topologies for shunt tuning are shown in Figures 2 and 3.



Fig.2 Type of Passive Filter



Fig.3 Passive tuned filters: (a) single-tuned (b) double-tuned

The shunt link with the power scheme of passive filters offers the least impedance path to the tuning frequency harmonic current. The filter series is intended to perform a complete load current compared to the shunt filter series while the passive shunt filter has a portion of the filter current in the series. They, therefore, need excessive protective devices. The series filter is comparatively more costly and therefore passive shunt filter is widely used as a harmonic filter. It also offers reactive power at the frequency of operation of the device

#### 4. Single Tuned Filter Design

The passive single-tuned filter is the most common filter used also, it is easy and cheaper than other ways to mitigate harmonic issues[5][21]. The most prevalent and cheap form of passive filter is the STF (Single Series Filter), it can be linked to the central distribution system in shunt and tune to a specific harmonic frequency with low impedance. Therefore, harmonic currents are diverted through the filter from the least impedance path. It is essential to select the suitable capacitor value for the design of the single tuned filter which allows excellent power factor at system frequency. The circuit diagram of the STF is shown in Figure. 3(a)

The correct choice of capacitor size is essential from a power factor point of perspective in the design of the filter. Equation 2 displays the relationship between capacitor reaction and reactive power:

$$Q = \frac{(V_{rms})^2}{XC} \tag{2}$$

where Vrms is the line voltage, Q is the reactive power in kVAR, and XC is the capacitive reactance of capacitor in ohms. From Equation 2, the value of capacitance (in Farad) is calculated as:

$$C = \frac{Q}{(2\pi f) \times (V_{rms})^2} \tag{3}$$

Where f is the fundamental frequency.

When the capacitive reaction is equivalent to inductive reaction as at a particular frequency, the resonance condition will occur as:

Then L can be found from Equation 5

$$L = \frac{1}{(2\pi f_n)^2 \times C} \tag{5}$$

$$f_n = f(FundamentalFreq.) \\ \times n$$

Where n=2, 3, 4, 5, 6...etc

While the resistance (R) can be calculated using Equation 6.

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$$Qc = \sqrt{\frac{L/c}{R}}$$
(6)

Notice that for this type of filter the quality factor is given as[22]:

$$50 < Q_c < 150$$

#### 5. Double Tuned Filter Design

The conventional double-tuned filter is composed of the series resonance circuit and parallel resonance circuit. The structure of this type of filter is shown in Figure.2(b)[23].

The series resonance circuit  $(L_1, C_1)$  and parallel resonance circuit  $(L_2, C_2)$  have resonance frequencies  $\omega_s$  and  $\omega_p$  respectively. They can be formulated as:

$$\begin{bmatrix} \omega_s \\ \omega_p \end{bmatrix} = \begin{bmatrix} \frac{1}{\sqrt{L_1 C_1}} \\ \frac{1}{\sqrt{L_2 C_2}} \end{bmatrix}$$
(7)

The impedance of this filter is:

$$Z = j\omega L_1 + \frac{1}{j\omega C_1} + \left(j\omega C_2 + \frac{1}{j\omega L_2}\right)^{-1}$$
(8)

$$Z = \frac{\left(1 - \frac{\omega^2}{\omega_s^2}\right) \left(1 - \frac{\omega^2}{\omega_p^2}\right) - \omega^2 L_2 C_1}{j \omega C_1 \left(1 - \frac{\omega^2}{\omega_p^2}\right)}$$
(9)

To calculate the parameters for this filter, two single-tuned filters connected in parallel are used with neglecting branch resistance as shown in Figure 4.



Fig. 4 Parallel two single tuned filter

The steps that describe the conversion method to calculate the filter parameter as described[23]. Starting from the resonance frequencies which can be obtained from Equation 10 for both branches shown in Figure 4.

$$\begin{bmatrix} \omega_a \\ \omega_b \end{bmatrix} = \begin{bmatrix} \frac{1}{\sqrt{L_a C_a}} \\ \frac{1}{\sqrt{L_b C_b}} \end{bmatrix}$$
(10)

While the impedance of the two-parallel single tuned can be expressed as:

$$Z_{ab} = \left( \left( j\omega La + \frac{1}{j\omega C_a} \right)^{-1} + \left( j\omega Lb + \frac{1}{j\omega C_b} \right)^{-1} \right) \quad (-1)$$

$$Z_{ab} = \frac{(1 - \frac{\omega^2}{\omega_a^2})(1 - \frac{\omega^2}{\omega_b^2})}{j\omega C_a \left(1 - \frac{\omega^2}{\omega_b^2}\right) + j\omega C_b (1 - \frac{\omega^2}{\omega_a^2})}$$
(12)

Both configuration in Figure 2 (b) and 4 are equivalent, that means they have equal impedance  $Z=Z_{ab}$ . From Equations 9 and 12 it can be seen that both have the same constant term of 1, so they have equal molecular and denominator. After analysing the coefficients of order  $\omega^4$  the following equation can be obtained:

$$\omega_a \omega_b = \omega_s \omega_p \tag{13}$$

And for coefficients of order  $\omega$ :

$$C_1 = C_a + C_b \tag{14}$$

While for coefficients of order  $\omega^3$  it can be found as:

$$C_b \frac{1}{\omega_a^2} + C_a \frac{1}{\omega_b^2} = C_1 \frac{1}{\omega_p^2}$$
(15)

The inductance  $L_1$  can be found from Equations 16:

$$L_1 = \frac{1}{C_a \omega_a^2 + C_b \omega_b^2} \tag{16}$$

Appling C<sub>1</sub>, L<sub>1</sub> in Equation 5 and using Equation 13 to obtain $\omega_s$  and  $\omega_p$  also, since  $\omega_a$  represent the

zero of double-tuned filter impedance then Z  $(\omega_a)=0$  as shown in Equation 17:

$$\left(1 - \frac{\omega_a^2}{\omega_s^2}\right) \left(1 - \frac{\omega_a^2}{\omega_p^2}\right) - \omega_a^2 L_2 C_1 = 0 \qquad (17)$$

From Equation, 18  $L_2$  can be obtained:

$$L_2 = \frac{\left(1 - \frac{\omega \bar{a}}{\omega s}\right) \left(1 - \frac{\omega \bar{a}}{\omega p}\right)}{\omega a^2 C_1} \tag{18}$$

Then from Equations13, 18, the value of  $C_2$  can be found using the following equation:

$$C_2 = \frac{1}{L_2 \omega_p^2} \tag{19}$$

# 6. Second-order high pass passive filter design

This type of filter consists of a capacitor that is connected in series with a parallel branch that includes inductance and resistance as shown in Figure 2. The filter elements are sized such that the filter works below the tuning frequency like the single-tuned filter and like the first-order high-pass filter at high frequencies. The behaviour of this filter acts like small inductive reactance in low frequencies bypassing the resistive branch and as a large inductive reactance in high frequencies diverting the current to the resistor branch. At the tuning frequency, a notch can be observed. This performance is achieving when the capacitor is tuned to the desired frequency with the inductor. The impedance is given by Equation 20[19][21][24]:

$$Z = \frac{RLCS^2 + LS + R}{S(CLS + CR)}$$
(20)

To calculate the parameters for this type the following equations are used[25].

$$f_n = \frac{1}{2\pi \times C \times R} \tag{21}$$

$$\therefore R = \frac{1}{2\pi f_n \times C} \tag{22}$$

$$m^2 = \frac{L}{R^2 C} \tag{23}$$

$$\therefore L = m^2 R^2 C \tag{24}$$

Where m is a factor that changes the inductance value of this type of filter. When the inductance value increases, the filter action is approached from the low frequencies, and when the inductance value is reduced, the filter action is approaching the high frequencies; m is a factor that changes the filter's working point only. The practical value of this factor is 0.5 < m < 2 [24]

## 7. Filter Configuration Selection

At lower harmonic frequencies, most waveforms have a significant proportion of harmonic distortion compared to elevated harmonic frequencies. That is why singletuned filters are intended in the first stage to restrain the frequencies that deal with lower harmonic while DTF is used in the second stage.

Four STF are used to restrain 5<sup>th</sup>, 7<sup>th</sup>, 11<sup>th</sup>, and 13<sup>th</sup> harmonics of six pulse ac to dc converter, while a one-second high-pass filter is used to eliminate high-order frequencies. Then the four single-tuned filters are converted to two double-tuned filters (5<sup>th</sup>&7<sup>th</sup>) and (11<sup>th</sup>& 13<sup>th</sup>).

# 8. Simulation and Results

In this study, three-phase full-wave uncontrolled six diodes AC to DC converters that are charging an electric car represented by a battery in the form of RLE has been simulated before and after using suggest passive filters in the MATLAB/Simulink environment. Table 1 present the circuit parameters used in the simulation and the complete simulation diagram as explained in Figure 5.

Table1.	Simulation	Parameters
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Table1. Simulation Tarameters.				
Parameter	Value			
Supply voltage (L-L)	11000 V			
Supply frequency F	50 Hz			
Source impedance	$1 \mathrm{m}\Omega$ , $0.1 \mathrm{mH}$			
(Rs, Ls)				
Load (electric car	300 V,50 A, 2			
battery RLE)	Ω, 10mH			
Charger Output	450 V,75 A			
Voltage and Current				
Three Phase	100 KVA, 50H			
Transformer Yg/Yg	,11000/483 V			



Fig .5 Uncontrolled Six-diode Converter

The system studied in three cases as elucidating:

## Case 1: Without Filters

This case represents the simulated circuit without any filters. The output voltage and current, in this case, are as shown in Figure 6, while the input voltage and current are explained in Figure 7.



Fig.6 Output voltage (VL) and output current (IL) for six diodes rectifier



Fig.7 Three-phase input voltage and current for six diode rectifiers

From Figure 6, it can be noticed that the output ripple for both voltage and current is too high and not acceptable according to standards. While Figure 7, shows that the input current for this case is wholly distorted with THD = 25.56 % as indicated by the FFT analysis in Figure 8.



Fig.8 FFT analysis for the input current in six diodes rectifier

## Case 2: With LC output filter

As explained earlier an LC filter is used to reduce the output ripple factor RF while the value of L and C can be calculated by taking the FFT analysis for the output voltage or current. It can be noticed that the highest harmonics frequency is the 6<sup>th</sup> which equal to 300 Hz as shown in Figure 9.



Fig.9 FFT analysis for output current

By using Equation 5 at f=300 Hz with chosen C=470mF, the value of L=59.88  $\mu$ H. Depending on this value, the circuit diagram transforms to Figure 10.



Fig.10 Six diodes with an LC output filter

The ripple factor of the output voltage and current after the connection of the LC filter becomes less than 3% as explained in Figure 11



Fig.11 Output voltage and current with output LC filter

Due to the connection of the LC filter at the output side, the input current becomes more distorted, and the %THD increased from 25.56% to 46.19% as shown in Figure 12 and Figure 13.



Fig.12 Input voltage and current with output LC filter



Fig.13 FFT analysis of the input current with output LC filter

Case 3: With both output and input filters

In this case, two types of the passive filter were designed depending on the following values: Total VAR in the system Q =19.272 KVAR, Quality factor =70, m=2.

• Single tuned passive filter (STPF) designed for the harmonics 5, 7,11, and 13 while high pass passive filter designed for harmonics 17 to ∞ and the filter parameters is as shown in Table 2.

Table2: STPF with HTPF parameters for six	
diodes with a car.	

Harmonic order	Type of filter	C (μF)	L (mH)	R (ohm)
5		3.0416	133.2	2.7908
7	SINGLE	3.0416	68.0	1.9934
11	TUNED	3.0416	27.5	1.2685
13	FILTER (STPF)	3.0416	19.7	1.0734
17-∞	HIGH TUNED FILTER	3.0416	5.8	61.5607

By simulating the parameters mentioned in Table 2, the system filter set can obtain as described in Figure 14 and the circuit diagram after the filter connection is illustrated in Figure15







Fig.15 Six diodes rectifier with input and output filters

 A double-tuned passive filter (DTPF) is designed by converting STPF to DTPF. Every two arms of STPF will be turned to one arm DTPS in this case for 5, 7, and 11,13, the two DTPF arms can be used while for 17 to ∞ still using high pass filter. The parameters for DTPF are described in Table 3.

Table3: DTPF Parameters.

Order	Filter				
	Туре	C1	L1	C2	L2
		(μF)	(mH)	(μF)	(mH)
5&7	DTE	6.08	45	57.83	5.3
11&13	DIF	6.08	11.5	222	0.323

Referring to the circuit mention in Figure 15 and using a filter system explained in Figure 14, the output voltage and current are constant as described in Figure 11 while the input voltage and current are shown in Figure 16. Applying FFT analysis for the input current results obtained shows that the THD decreased from 46.19 % as apparent in Figure 13 to 5.81 % as shown in Figure17 which is still higher than the value of the standards of 5%.



Fig.16 Input voltage and current for six diodes rectifier with STPF +HTPF



Fig.17 FFT analysis of the input current with an input filter

To solve this issue and reduce the THD below 5% the STPF is converted to double-tuned passive filter DTPF.

The output voltage and current are the same in Figure 11 while the input voltage and current are as shown in Figure 18



Fig.18 Input voltage and current with DTPF filter

After taking the FFT analysis for the input current, the total harmonics distortion THD is reduced from 46.19 % to 3.73% as shown in Figure 19. This confirmed the effectiveness of DTPF as compare with the STPF set to match the standards values of THD which is below 5% and the summarized variation of the %THD for this circuit is as shown in Figure 20.







Fig.20 Variation of the % THD for six diodes rectifier with car

#### 9. CONCLUSION

A PC-based design technique for prevalent kinds of passive filters output and input namely LC filter for the output and single, double-tuned filters and high pass filter for input has been presented in this study. The proposed LC filters reduced the output ripple factor to match the standards but increase the THD of the source current from 25.56% to 46.19%. While the combination of STPF with HTPF reduced the %THD from 46.19% to 5.81% in the simulation which was still out of the standards (<5%). On the other hand, the conversion of STPF to DTPF combined with DTPF decreases the THD percentage from 46.19% to 3.73% which met the IEEE 519 recommended harmonic standards. The scope of using such filters is the size of filters as compare with other types but it is very cheap.

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# تصميم مرشح غير فعال في الاخراج والادخال لمحطات شحن السيارات الكهربائية

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#### الملخص

يتزايد تأثير محطات شحن السيارات الكهربائية على التوافقيات الظاهرة في شبكة القدرة الكهربائية مع زيادة وجودها مما يخلق تشوه توافقي في نظام القدرة. في الوقت نفسه، تصبح جودة تيار وفولتية الشحن متدنية. لذلك، فإن تقليل عامل تموج الإخراج (RF) وتقليل التوافقيات في تيار الإدخال في هذه الحالة امر ضروري. يقدم هذا البحث تصميم مرشح الإخراج وثلاثة أنواع من مرشحات الإدخال غير الفعالة التي تربط على التوازي لتقليل التوافقيات المتولدة في انظمة القدرة الصناعية نتيجة وجود محطات الشحن. تم استخدام برنامج المحاكاة Simulink / المعالمة للتي تربط على التوازي لتقليل التوافقيات المتولدة وبعد تركيب الفلاتر وجد أن النسبة المئوية لعامل التموج ( RF%) لتيار وفولتية الإخراج والنسبة المئوية للتشوه التوافقي التوافقيات المتولدة لتتوافق مع المعايير المحددة.

#### الكلمات الدالة:

شاحنة السيارة الكهربائية، محطة الشحن، المرشحات غير الفعالة، التشوه التوافقي الكلي، تشوه التيار.